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User's Gride





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TX802SG SERIES TWO WIRE BRIDGE TRANSMITTER



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Czech Republic: ul. Rude armady 1868, 733 01 Karvina-Hranice, Czech Republic

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- 3. Repair instructions and/or specific problems relative to the product.

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The Proper Installation & Maintenance of TX802SG.

MOUNTING.

- (1) Mount in a clean environment in an electrical cabinet on DIN or EN mounting rail.
- (2) Do not subject to vibration or excess temperature or humidity variations.
- (3) Avoid mounting in cabinets with power control equipment.
- (4) To maintain compliance with the EMC Directives the LPI-B is to be mounted in a fully enclosed steel cabinet. The cabinet must be properly earthed, with appropriate input / output entry points and cabling.

WIRING.

- (1) All cables should be good quality overall screened INSTRUMENTATION CABLE with the screen earthed at one end only.
- (2) Signal cables should be laid a minimum distance of 300mm from any power cables.
- (3) For 2 wire current loops and 2 wire voltage signals or 2 wire current signals, Austral Standard Cables B5102ES is recommended. For 3 wire transmitters Austral Standard Cables B5103ES is recommended.
- (4) It is recommended that you do not ground current loops and use power supplies with ungrounded outputs.
- (5) Lightning arrestors should be used when there is a danger from this source.
- (6) Refer to diagrams for connection information.

COMMISSIONING.

- (1) Once all the above conditions have been carried out and the wiring checked apply power to the TX802SG loop and allow five minutes for it to stabilize.
- (2) Take a low (approx 10%) and high (approx 90%) reading of the variable being measured by the transducer supplying the signal to the TX802SG, and ensure that this agrees with the level being indicated by the PLC or indicator, etc, that the TX802SG is connected into. Adjust for any difference using the Zero & Span trimpots in the top of the TX802SG enclosure with a small screw driver, until the two levels agree. (Clockwise to increase the output reading and anti-clockwise to decrease the output reading.)

MAINTENANCE.

- Repeat (2) of Commissioning.
- (2) Do it regularly at least once every 12 months.

INPUT RANGES									
mV	IR	mV	IR						
0~1mV	1	0~200mV	21						
0~2mV	2	0~500mV	22						
0~3mV	3	0~750mV	23						
0~4mV	4	0~1000mV	24						
0~5mV	5	2~6mV	25						
0~6mV	6	5~10mV	26						
0~8mV	7	15~20mV	27						
0~10mV	8	30~35mV	28						
0~12mV	9	30~40mV	29						
0~15mV	10	30~45mV	30						
0~20mV	11	30~60mV	31						
0~25mV	12	50~80mV	32						
0~30mV	13	100~200mV	33						
0~35mV	14	-2~4mV	34						
0~40mV	15	-4~6mV	35						
0~50mV	16	-5~7mV	36						
0~60mV	17	-10~20mV	37						
0~75mV	18	-50~70mV	38						
0~80mV	19	-100~300mV	39						
0~100mV	20	-200~800mV	40						
Spe	Z								

TX802SG: 2 Wire Bridge Transmitter.

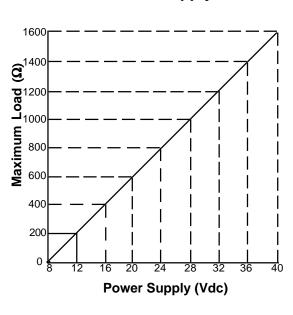
Isolating Bridge Input to 4~20mA Output Loop Powered Transmitter.

Features.

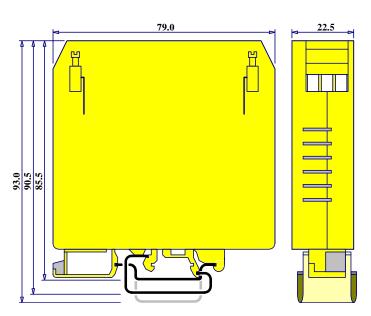
- Field Programmable Bi-Polar Input Ranges.
- Isolated Input to Output 2.0kVDC.
- High Accuracy.
- 40~200mV Output Test Signal.
- LED Indication of Loop Current.
- Selectable 3 Second Input Damping.
- Low Cost.
- Easy to Install.
- Compact DIN Rail Mount Enclosure.
- Available Standard or Special Calibration.
- Reverse Polarity Protection.
- Corrosion Proofed Circuit Board & Components by Isonel 642. (Except Terminals & DIP Switches.)



Graph Of Maximum Load Versus Power Supply.



Enclosure Dimensions.



Quality Assurance Programme.

The modern technology and strict procedures of the ISO9001 Quality Assurance Programme applied during design, development, production and final inspection grant the long term reliability of the instrument.

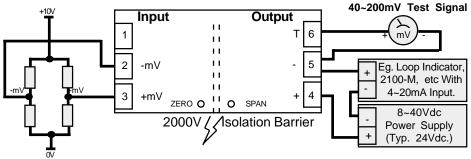
TX802SG Specifications.

1700230	opecifications.	
Input	-mV	Field Programmable: 1~1000mVdc and Bipolar.
		Minimum Input Resistance = $1M\Omega$.
		Maximum Over-range = 30Vdc Continuous.
		3 Second Input Damping Selectable With S3-1.
Output	-mA	2 Wire 4~20mA. (Loop Powered.)
	-mV	40~200mV ∝ 4~20mA. (Indicative Test Signal Only.)
		Other Output Voltages Available. eg 1~5V.
Power Supp	oly	8~40Vdc.
Supply Volt	age Sensitivity	<±0.005%/V FSO.
Output Load	d Resistance	800Ω @ 24Vdc. (50 Ω /V Above 8Vdc.)
Maximum Output Current		Limited to <28mA.
Accurate to		<±0.1% FSO Typical.
Linearity & I	Repeatability	<±0.1% FSO Typical.
Ambient Dri	ift	<±0.02%/C FSO Typical.
Noise Immu	unity	125dB CMRR Average. (2.0kVac RMS Limit.)
R.F. Immur	nity	<1% Effect FSO Typical.
Isolation Vo	ltage	2.0kVac/dc Input to Output for 60sec.
Response 7	Time	200msec Typical. (10 to 90% 50msec Typical.)
Operating T	emperature	0~70C.
Storage Ter	mperature	-20~80C.
Operating H	lumidity	90%RH Max. Non-Condensing.
Constructio	n	6.6 Polyamide Thermoplastic Rail Mount Enclosure.

Note 1. Specifications based on Standard Calibration Unit, unless otherwise specified.

Note 2. Due to ongoing research and development, designs, specifications, and documentation are subject to change without notification. No liability will be accepted for errors, omissions or amendments to this specification.

Examples of Input Connection.

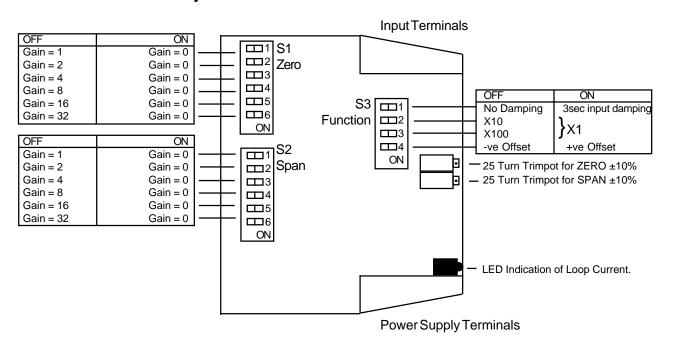


Terminations.

Input	1 2 3	NC -mV +mV
Output	4 5 6	+mA -mA mVTEST

Note: Only use precision regulated power supply for bridge power supply.

Plan View of TX802SG Adjustments.



TX802SG Input Programming.

If the input range is not listed in the programming table, use the following formulae to work out the Zero and Span DIP switch settings for gain.

Span Gain =_	60 X Pregain (Signal High - Signal Low)	
Zero Gain = _	5 x Signal Low Pregain	

EFFECTIVE INPUT RANGE (i.e. Signal High - Signal Low.)	S3-2	S3-3	Pregain
1mV <= Range <= 10mV	0	0	1
10mV < Range <= 100mV	0	1	10
100mV < Range <= 1000mV	1	1	100

If Zero gain is: 1/ -ve Put S3-4 ON (Positive Offset) 2/ +ve Put S3-4 OFF (Negative Offset)

Notes: (a) Enter ranges as their mV value. eg. Enter 100mV as 100.

(b) Use the same pregain value in both the Span and Zero gain formulae.

(c) Enter the Zero or Span gain value into the appropriate Zero or Span DIP switch.

Gain Value	1	2	4	8	16	32	So if a (ie, gai
DIP Switch No.	1	2	3	4	5	6	Dip sw

So if a gain value of 28 is required, put DIP switch No's 3, 4, 5 OFF (ie, gains of 4 + 8 + 16 = 28) and all the other DIP switches ON. Dip switches are accessed by seperating the two halves of the TX802SG enclosure

TX802SG Input Range Programming Table.

Notes: 1/ Switch status 1 = ON, 0 = OFF, X = DON'T CARE.

2/ Input ranges with '*' beside them require more adjustment with the Span trimpot.

input ranges with	S4 70% S2 Span S2 Function															
INPUT RANGE	S1-Zero S2-Span 1 2 3 4 5 6 1 2 3 4 5							6	S3-Function			_				
	<u> </u>					6	_					6	1	2	3	4
0~1mV 0~2mV	1	1	1	1	1	1	1	0	0	0	0	0	O	0	0	X
0~211V 0~3mV	1	1	1	1	1	1	1	1	0	1	0	1	Ž	0	0	X
0~3mV 0~4mV	1	1	1	1	1	1	0	0	0	0	1	1	ĪĒ	0	0	X
0~4111V 0~5mV	1	1	1	1	1	1	1	1	0	0	1	1	Σ P	0	0	X
0~5mV	1	1	1	1	1	1	1	0	1	0	1	1	١₹	0	0	X
0~8mV*	1	1	1	1	1	1	<u> </u>	1	1	0	1	1		0	0	X
0~10mV	1	1	1	1	1	1	1	0	0	1	1	1	၂ ပ	0	0	X
0~10mV	1	1	1	1	1	1	1	0	1	1	0	0	Φ	0	1	X
0~15mV	1	1	1	1	1	1	1	1	1	0	1	0	3s	0	1	X
0~20mV	1	1	1	1	1	1	1	0	0	0	0	1	1 ' '	0	1	X
0~25mV	1	1	1	1	1	1	1	1	1	0	0	1	씸	0	1	X
0~30mV	1	1	1	1	1	1	1	1	0	1	0	1		0	1	X
0~35mV	1	1	1	1	1	1	0	1	1	1	0	1	I	0	1	X
0~40mV	1	1	1	1	1	1	0	0	0	0	1	1	7	0	1	X
0~50mV	1	1	1	1	1	1	1	1	0	0	1	1	0	0	1	X
0~60mV	1	1	1	1	1	1	1	0	1	Ō	1	1	IΥ	0	1	X
0~75mV	1	1	1	1	1	1	1	1	1	0	1	1	l⊨.	0	1	X
0~80mV*	1	1	1	1	1	1	1	1	1	0	1	1	۱ <u>۵</u> .	0	1	X
0~100mV	1	1	1	1	1	1	1	0	0	1	1	1	S	0	1	Х
0~200mV	1	1	1	1	1	1	1	0	0	0	0	1		1	1	Х
0~500mV	1	1	1	1	1	1	1	1	0	0	1	1	<u> (</u>	1	1	Х
0~750mV	1	1	1	1	1	1	1	1	1	0	1	1	Z	1	1	Χ
0~1000mV	1	1	1	1	1	1	1	0	0	1	1	1		1	1	Χ
2~6mV	1	0	1	0	1	1	0	0	0	0	1	1	АМР	0	0	0
5~10mV	0	1	1	0	0	1	1	1	0	0	1	1		0	0	0
10~15mV	1	0	1	1	0	0	1	1	0	0	1	1		0	0	0
10~20mV	1	0	1	1	0	0	1	0	0	1	1	1	lo	0	0	0
10~30mV	0	1	0	1	1	1	1	0	0	0	0	1		0	1	0
30~45mV	0	0	0	0	1	1	1	1	1	0	1	0	<u> </u>	0	1	0
30~60mV	0	0	0	0	1	1	1	1	0	1	0	1		0	1	0
50~80mV	0	1	1	0	0	1	1	1	0	1	0	1		0	1	0
100~200mV	1	0	1	1	0	0	1	0	0	1	1	1	١. ا	0	1	0
-2~4mV	1	0	1	0	1	1	1	0	1	0	1	1	9	0	0	1
-4~6mV	1	1	0	1	0	1	1	0	0	1	1	1		0	0	1
-6~6mV	0	0	1	1	1	1	1	0	1	1	0	0	<u>P</u>	0	1	1
-10~20mV	0	1	0	1	1	1	1	1	0	1	0	1	Ι.	0	1	1
-60~60mV	0	0	1	1	1	1	1	0	1	1	0	0	╚	1	1	1
-100~300mV	0	1	0	1	1	1	0	0	0	0	1	1	S	1	1	1
-200~800mV	1	0	1	0	1	1	1	0	0	1	1	1	. ,	Ί	1	