

User's Guide

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CIO-CTR10HD
CIO-CTR20HD

EC Declaration of Conformity

CIO-CTR10HD&20HD	Counter / Timer Boards
Part Number	Description

to which this declaration relates, meets the essential requirements, is in conformity with, and CE marking has been applied according to the relevant EC Directives listed below using the relevant section of the following EC standards and other normative documents:

EU EMC Directive 89/336/EEC: Essential requirements relating to electromagnetic compatibility.

EU 55022 Class B: Limits and methods of measurements of radio interference characteristics of information technology equipment.

EN 50082-1: EC generic immunity requirements.

IEC 801-2: Electrostatic discharge requirements for industrial process measurement and control equipment.

IEC 801-3: Radiated electromagnetic field requirements for industrial process measurements and control equipment.

IEC 801-4: Electrically fast transients for industrial process measurement and control equipment.

Carl Haapaoja, Director of Quality Assurance

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QUICK START

The CIO-CTR10HD and CIO-CTR20HD are easy to use. Here is the quick start procedure for those who know how to open the PC and install expansion boards, and want to dive right in. The CIO-CTR20HD is really a single board with two CIO-CTR10HD's on it. For the balance of this manual we will refer to both boards as the CIO-CTRHD, and will only use the complete board name in instances where there are items specific to one board or the other.

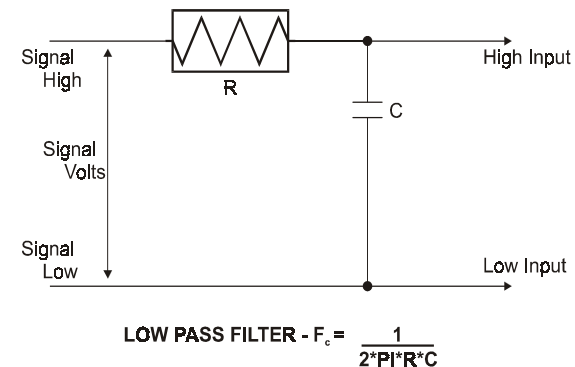
This quick start procedure will help you quickly and easily setup, install and test your board. We assume you already know how to open the PC and install expansion boards. If you are unfamiliar or uncomfortable with board installation, please refer to your computer's documentation. Though we recommend the use of InstaCal to guide you through your installation, detailed written instructions are provided in the next chapter.

We recommend you perform the software installation described in sections below prior to installing the board in your computer. The InstaCal™ operations below will show you how to properly set the switches and jumpers on the board prior to physically installing the board in your computer.

INSTALLING INSTACAL™

Windows (in its various forms) and DOS users install the program by running the INSTALL.EXE program supplied on your InstaCal diskette (some releases of InstaCal will provide SETUP.EXE rather than Install.EXE. Use SETUP.EXE if it is included on your InstaCal disk). It will create all required folders/directories and unpack the various pieces of compressed software. Simply run install/setup and follow the on-screen instructions. Note where the installed files are placed, as you will need to access them in the next step (the default location is on your main hard drive in a directory or folder named C:\CB).

The key term in a low pass filter circuit is **cut off frequency**. The cut off frequency is that frequency above which no variation of voltage with respect to time may enter the circuit. For example, if a low pass filter had a cut off frequency of 30 Hz, the kind of interference associated with line voltage (60Hz) would be filtered out but a signal of 25Hz would be allowed to pass.



Also, in a digital circuit, a low pass filter might be used to de-bounce an input from a momentary contact button pushed by a person.

A low pass filter may be constructed from one resistor (R) and one capacitor (C). The cut off frequency is determined according to the formula:

Where Pi = 3.14...

$$F_c = \frac{1}{2 * \text{Pi} * R * C}$$

$$R = \frac{1}{2 * \text{Pi} * C * F_c}$$

$$2 = \frac{10K + 10K}{10K}$$

$$R1 = (A - 1) * R2$$

For example, if the signal varies between 0 and 20 volts and you wish to measure that with an input with a full scale range of 0 to 10 volts, the *Attenuation* is 2:1 or just 2.

For a given attenuation, pick a handy resistor and call it R2, then use this formula to calculate R1.

Digital inputs also make use of voltage dividers, for example, if you wish to measure a digital signal that is at 0 volts when off and 24 volts when on, you cannot connect that directly to the CIO-CTR digital inputs. The voltage must be dropped to 5 volts max when on. The Attenuation is 24:5 or 4.8. Use the equation above to find an appropriate R1 if R2 is 1K. Remember that a TTL input is 'on' when the input voltage is greater than 2.5 volts.

IMPORTANT NOTE

The resistors, R1 and R2, are going to dissipate all the power in the divider circuit according to the equation Current = Voltage / Resistance. The higher the value of the resistance (R1 + R2) the less power dissipated by the divider circuit. Here is a simple rule:

For Attenuation of 5:1 or less, no resistor should be less than 10K.

For Attenuation of greater than 5:1, no resistor should be less than 1K.

The CIO-TERMINAL has the circuitry on board to create custom voltage dividers. The CIO-TERMINAL is a 16" by 4" screw terminal board with two 37 pin D type connectors and 56 screw terminals (12 - 22 AWG). Designed for table top, wall or rack mounting, the board provides prototype, divider circuit, filter circuit and pull-up resistor positions which you may complete with the proper value components for your application.

LOW PASS FILTERS DE-BOUNCE INPUTS

A low pass filter is placed on the signal wires between a signal and an A/D board. It stops frequencies greater than the cut off frequency from entering the A/D board's analog or digital inputs.

RUNNING INSTACAL™

To run *InstaCal™* in the various forms of Windows, find the file named InstaCal.exe using your file management system and double click your mouse on it. In DOS simply type *instacal* and press the *Enter* key.

Once running, *InstaCal™* provides four sub-menus (plus exit).

1. Select **Install** (either highlight it and hit enter or double click your mouse on it).
2. Select **Board #0** (select another number if Board #0 is already installed)
3. Select **Board Type**
4. Move through the selections and highlight the particular board you are installing (e.g. CIO-CTR10HD or CIO-CTR20HD). Double click on the board or hit enter.
5. The board's default settings are then displayed. The board's defaults are:

BASE ADDRESS:	300H (768 Decimal) Same as data sheet. If address 300 is not available, choose another and InstaCal will show you the correct switch setting.
WAIT STATE:	OFF.
6. You are now ready to install the board in your computer. Turn off your computer, unplug it from AC power, open your PC and install the board in any unused ISA slot. After the board is installed and the computer is closed up, turn the power back on.
7. Run *InstaCal™* again, and at the main menu select **Test**.

a. Select the board you just installed

b. Select **Internal Test**

c. The internal control registers of the board will then be tested. If this test is successful, your board is installed correctly. If not, you likely have a base address conflict, or have the base address switch set incorrectly. Please refer to the next chapter for more information regarding selecting and setting the base address.

d. If the **Internal Test** is completed successfully, you may want to check that the I/O pins are working correctly. To check this select **External Test** and follow the instruction provided. This will

require you to use the shorting wires supplied with the board to short inputs to outputs for I/O testing.

VOLTAGE DIVIDERS

If you wish to measure a signal which varies over a range greater than the input range of a digital input, a voltage divider can drop the voltage of the input signal to the level the digital input can measure.

A voltage divider takes advantage of Ohm's law, which states,

$$\text{Voltage} = \text{Current} * \text{Resistance}$$

and Kirchoff's voltage law which states,

The sum of the voltage drops around a circuit will be equal to the voltage drop for the entire circuit.

Implied in the above is that any variation in the voltage drop for the circuit as a whole will have a proportional variation in all the voltage drops in the circuit.

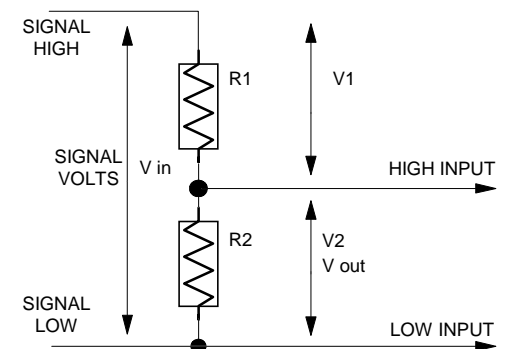
A voltage divider takes advantage of the fact that the voltage across one of the resistors in a circuit is proportional to the voltage across the total resistance in the circuit.

The trick to using a voltage divider is to choose two resistors with the proper proportions relative to the full scale of the digital input and the maximum signal voltage.

The phenomena of dropping the voltage proportionally is often called attenuation. The formula for attenuation is:

$$\text{Attenuation} = \frac{R1 + R2}{R2}$$

The variable *Attenuation* is the proportional difference between the signal voltage max and the full scale of the input.



$$\text{SIMPLE VOLTAGE DIVIDER} - \frac{V_{in}}{V_{out}} = \frac{R1 + R2}{R2}$$

SPECIFICATIONS

POWER CONSUMPTION

+5V Supply	10HD: 475mA typical, 590mA max 20HD: 825mA typical, 1AMP max
+12V Supply	None
-12V Supply	None

NOTE

Additional power will be drawn by user's connections to the power pins accessible on CIO-CTR connectors.

COUNTER I/O

9513 Output Low	0.4 V Max @ 3.2 mA
9513 Output High	2.4 V Min @ -200 uA
9513 Input Low	-0.5V Min, 0.8V Max
9513 Input High	2.2V Min, +5VMax

INTERRUPT INPUT

Type	Positive Edge Triggered
PC Bus IRQ	IRQ2 - IRQ7
Enable	Base +400

ENVIRONMENTAL

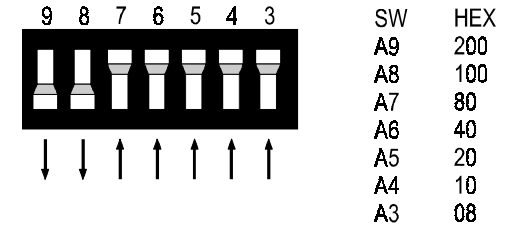
Operating Temperature	0 - 50 Deg C
Storage Temperature	-20 to 70 Deg C
Humidity	0 to 90% Non-Condensing
Weight	5 Oz.

DETAILED INSTALLATION

The CIO-CTRHD has one bank of switches, a base address switch which must be set before installing the board in your computer. The InstaCal program included with the CIO-CTR will show how these switches are to be set and may be run before you install the board, or you may proceed from the information here.

BASE ADDRESS

Unless there is already a board in your system which uses address 300 HEX (768 Decimal) then you can leave the switches as they are set at the factory.



In the example shown here, the CIO-CTR is set for base address 300H (768 Decimal).

BASE ADDRESS SWITCH - Address 300H shown here.

Certain address are used by the PC, others are free and may be used by the CIO-CTRHD and other expansion boards. We recommend BASE = 300H (768D) be tried first.

HEX RANGE	FUNCTION	HEX RANGE	FUNCTION
000-00F	8237 DMA #1	2C0-2CF	EGA
020-021	8259 PIC #1	2D0-2DF	EGA
040-043	8253 TIMER	2E0-2E7	GPIB (AT)
060-063	8255 PPI (XT)	2E8-2EF	SERIAL PORT
060-064	8742 CONTROLLER (AT)	2F8-2FF	SERIAL PORT
070-071	CMOS RAM & NMI MASK (AT)	300-30F	PROTOTYPE CARD
080-08F	DMA PAGE REGISTERS	310-31F	PROTOTYPE CARD
0A0-0AF	8259 PIC #2 (AT)	320-31F	HARD DISK (XT)
0A0-0A1	NMI MASK (XT)	378-37F	PARALLEL PRINTER
0C0-0DF	8237 #2 (AT)	380-38F	SDLC
0F0-0FF	80287 NUMERIC CO-P (AT)	3A0-3AF	SDLC
1F0-1FF	HARD DISK (AT)	3B0-3BB	MDA
200-20F	GAME CONTROL	3BC-3BF	PARALLEL PRINTER
210-21F	EXPANSION UNIT (XT)	3C0-3CF	EGA

HEX RANGE	FUNCTION	HEX RANGE	FUNCTION
23C-23F	ALT BUS MOUSE	3E8-3EF	SERIAL PORT
270-27F	PARALLEL PRINTER	3F0-3F7	FLOPPY DISK
2B0-2BF	EGA	3F8-3FF	SERIAL PORT

The CIO-CTRHD BASE switch may be set for address in the range of 000-3F8 so it should not be hard to find a free address area for your CIO-CTRHD. Once again, if you are not using IBM prototyping cards or some other board which occupies these addresses, then 300-31F HEX are free to use. Address not specifically listed, such as 390-39F, are usually free.

INTERRUPT LEVEL SELECT

A single interrupt input at pin 26 allows you to initiate an interrupt service routine with an external pulse. The interrupt level is selected via software and is one of the options set in the InstaCal configuration file.

CLOCK SOURCE SELECT

The source of the pulses supplied to the 9513 for timing operations is programmable and there are a number of options. These options are shown in the InstaCal INSTALL screen.

INSTALLING THE CIO-CTR IN THE COMPUTER

Turn the power off. Remove the cover of your computer. Please be careful not to dislodge any of the cables installed in your computer as you slide the cover off.

Locate an empty expansion slot in your computer.

Push the board firmly down into the expansion bus connector. If it is not seated fully it may fail to work and could short circuit the PC bus power onto a PC bus signal. This could damage the motherboard in your PC as well as the CIO-CTRHD.

The CIO-CTRxxHD connector is a 50 pin header type connector. All the signals from the 9513 and interrupt are accessible. Pin one is designed by a square solder pad. It is the pin on the PC Bus end of the connector, and closest to the edge of the card.

Cables C50FF-2 and the CIO-MINI50 allow easy connection to all of the counter signals through 12-22 AWG screw terminals. The CIO-CTR20HD has two of these connectors, P1 and P2. The signals on each are identical.

BASE +400 & +401

D7	D6	D5	D4	D3	D2	D1	D0
WSEN	CLK11	CLK10	CLK01	CLK00	L2	L1	L0

Read - No read function for this register.

Write - L0, L1, and L2 select IRQ as follows:

L2	L1	L0	IRQ
0	0	0	N/A
0	0	1	N/A
0	1	0	2
0	1	1	3
1	0	0	4
1	0	1	5
1	1	0	6
1	1	1	7

CLK0# selects the clock source for the 9513 designated "U1." CLK1# selects the clock source for the 9513 designated "U2."

CLK#1	CLK#0	CLOCK SOURCE
0	0	1 MHz
0	1	5 MHz
1	0	External
1	1	N/A

WSEN enables or disables the wait state.

1 = Wait State Enabled
0 = Wait State Disabled

NOTE: Base +400 applies to CTR10HD and the first ten channels of CTR20 HD. Base +401 applies to CTR20HD only and the second ten channels.

I/O CONNECTOR & PINOUT

To write to or read from a register in decimal or HEX, the following weights apply:

BIT POSITION	DECIMAL VALUE	HEX VALUE
0	1	1
1	2	2
2	4	4
3	8	8
4	16	10
5	32	20
6	64	40
7	128	80

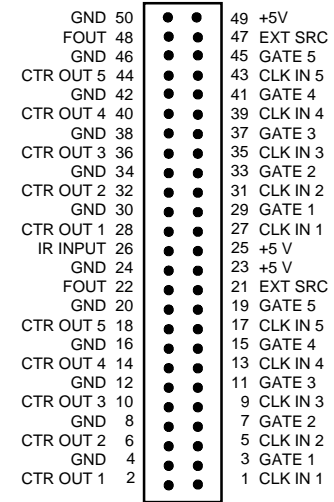
To write control or data to a register, the individual bits must be set to 0 or 1 then combined to form a Byte. Data read from registers must be analyzed to determine which bits are on or off.

The method of programming required to set/read bits from bytes is beyond the scope of this manual. It will be covered in most Introduction To Programming books, available from a bookstore.

In summary form, the registers and their function are listed on the following table. Within each register are 8 bits which may constitute a byte of data or 8 individual bit functions.

ADDRESS	READ FUNCTION	WRITE FUNCTION
BASE +0	Data from 9513 #1	Data for 9513 #1
BASE +1	Status of 9513 #1	Commands to 9513 #1
BASE +2	Data from 9513 #2	Data for 9513 #2
BASE +3	Status of 9513 #2	Commands to 9513 #2
BASE +400	No read function.	Wait State, Interrupt and Counter Source (1st ten channels)
CTR20 only		
BASE +4	Data from 9513 #3	Data for 9513 #3
BASE +5	Status of 9513 #3	Commands to 9513 #3
BASE +6	Data from 9513 #4	Data for 9513 #4
BASE +7	Status of 9513 #4	Commands to 9513 #4
BASE +401	No read function.	Wait State, Interrupt and Counter Source (2nd ten channels)

The CIO-CTRxxHD use a 50-pin inline connector. The CIO-CTR10HD has only one connector while the CIO-CTR20HD has two. The connectors are compatible with the optional CFF50-series cables and CIO-MINI50 screw terminal adapter board. The pinout of these connectors is shown below.



CIO-CTRxxHD CONNECTOR

Each CIO-CTRHD boards is supplied with the InstaCal installation, calibration and test package. Use it to guide the installation procedure. InstaCAL installation is described in chapter one (Quick Start).

The CIO-CTRHD family is fully supported by the powerful Universal Library package. Details regarding installation and usage of the Universal Library software can be found in the Universal Library documentation. Please note that InstaCal also creates a configuration file required for programmers who use the Universal Library programming libraries.

For those programmers writing drivers of their own, a complete register description follows.

CONTROL & DATA REGISTERS

The CIO-CTR10HD is composed of two AM9513 counter timer chips, the CIO-CTR20HD is composed of four. Each 9513 contains five counters of 16 bits each. Associated with each counter are an input source, a count register, a load register, a hold register, an output and a gate. The 9513 is extremely flexible and this flexibility can make it a challenge to program the chip directly.

Unlike an Intel 8254 which has a single source, single gate and unique I/O address for each counter, the 9513 is fully programmable and any counter may be internally connected to any gate and receive it's counts from a number of sources. In addition, each counter does not have a unique I/O address. The 9513 takes only two address per chip, one of which is a data path to the counter's load and hold registers.

There is no 9513 register information in this manual. Those wishing to know more about the AM9513 and its programming should request the manual from our technical support group. As of this writing there is no charge for the manual.

We strongly suggest that you use the Universal Library, rather than resort to programming the 9513 directly. It is difficult to program and because programming support is available through the Universal Library, we cannot help with other 9513 programming

The CIO-CTR is an I/O mapped expansion board. The CIO-CTR20HD occupies 8 I/O addresses and the CIO-CTR10HD occupies 4 addresses.

The first address, or BASE ADDRESS, is determined by setting a bank of switches on the board.

Most of the functions that this board is capable of performing can be achieved using the Universal Library. Unless you have a good reason for direct register manipulation, we suggest you use the Universal Library.

The register descriptions follow the format:

7	6	5	4	3	2	1	0
A7	A6	A5	A4	A3	A2	A1	A0

Where the numbers along the top row are the bit positions within the 8 bit byte and the numbers and symbols in the bottom row are the functions associated with that bit.